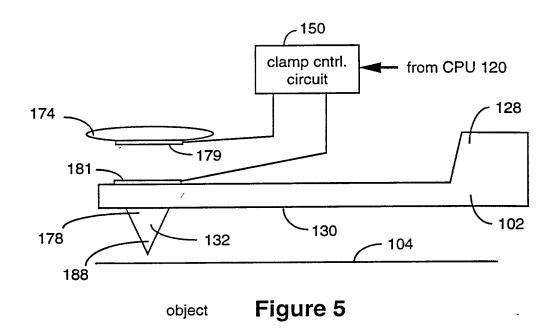
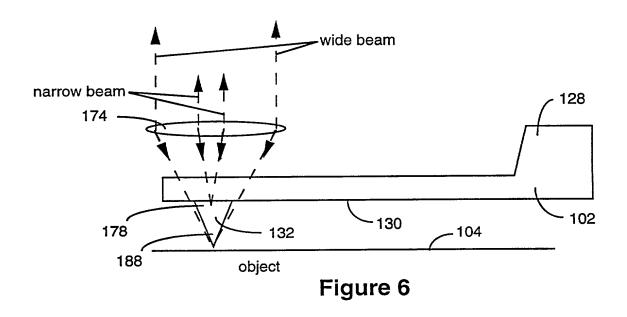


170 clamp cntrl. circuit from CPU 120 128 132 130 102 object

Figure 4b





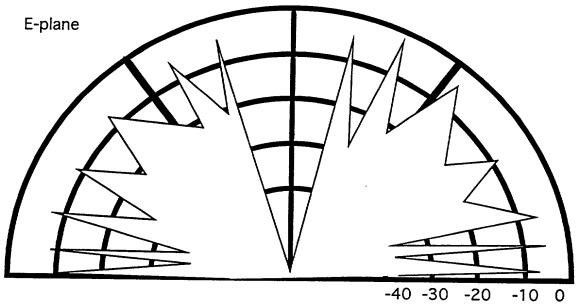


Figure 7a

gain in decibels (subtract -40 db)

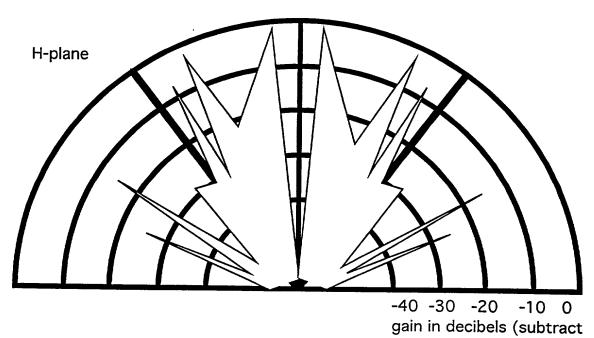
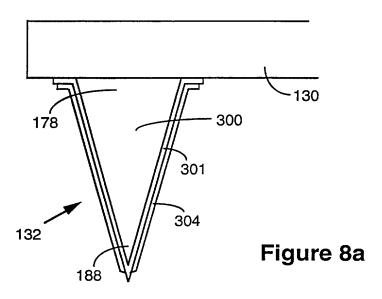
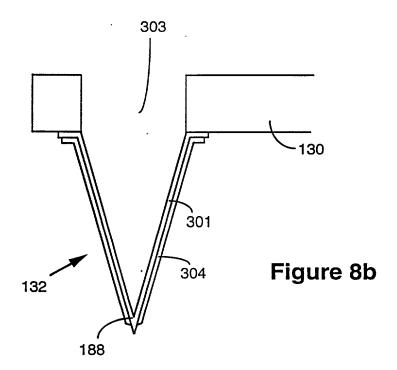
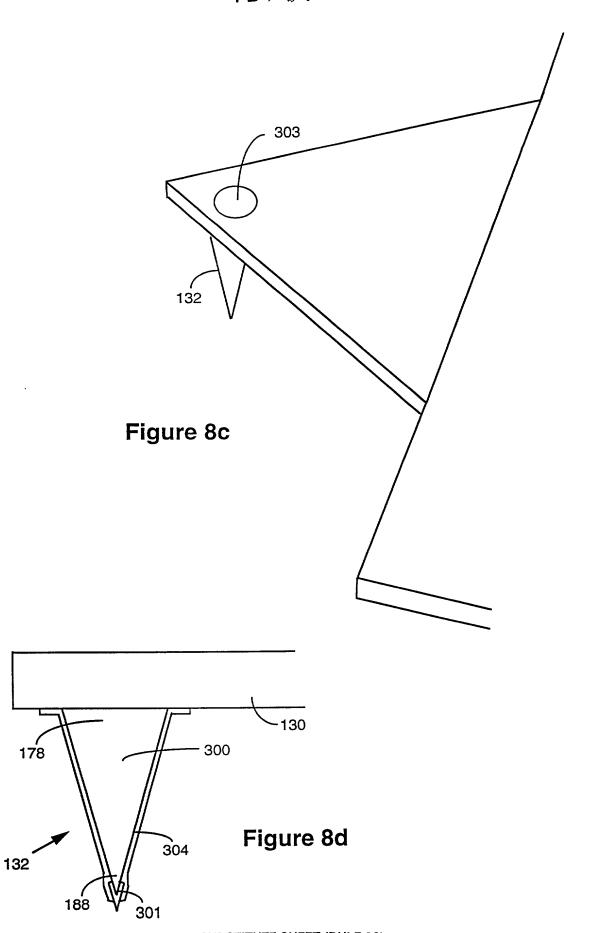


Figure 7b

-40 db)







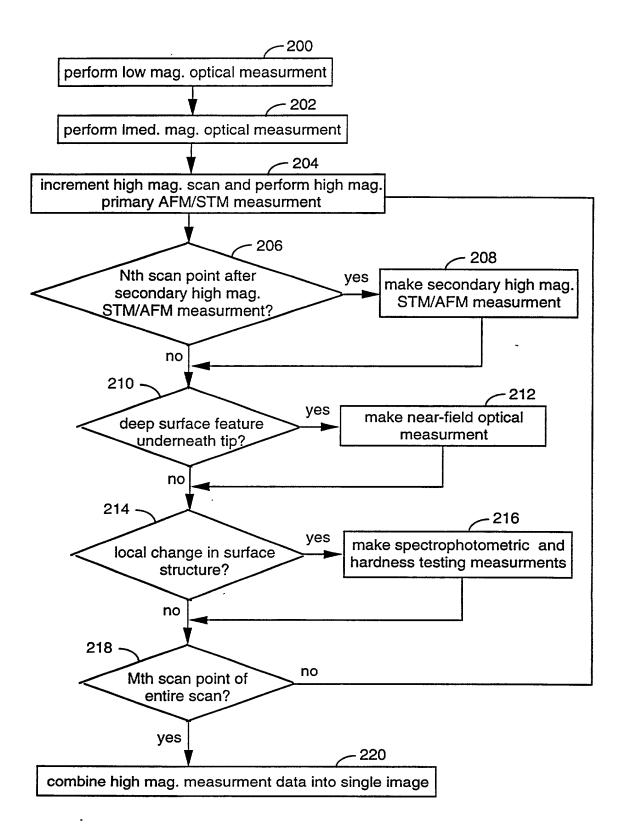


Figure 9

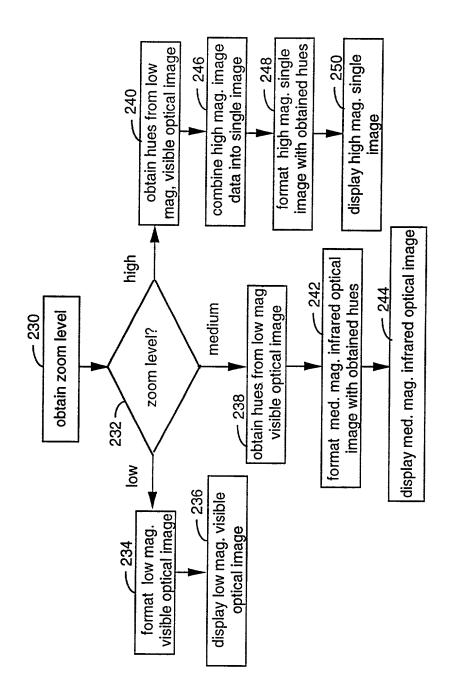


Figure 10a

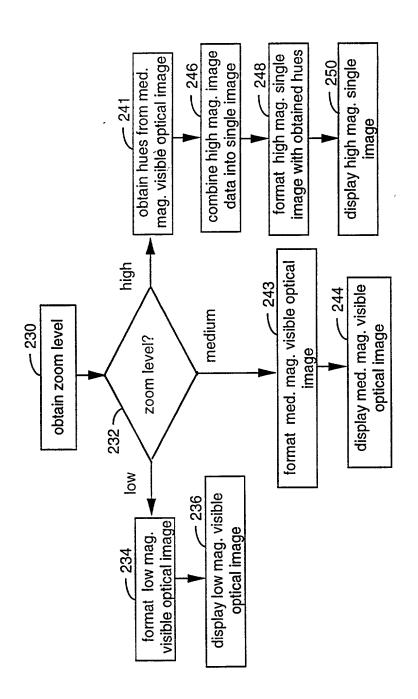
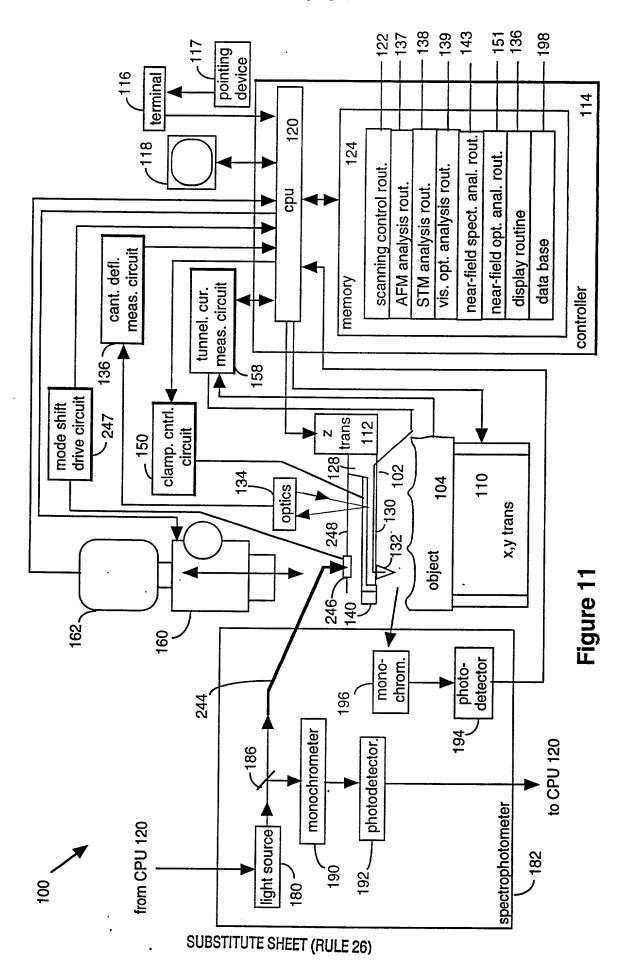
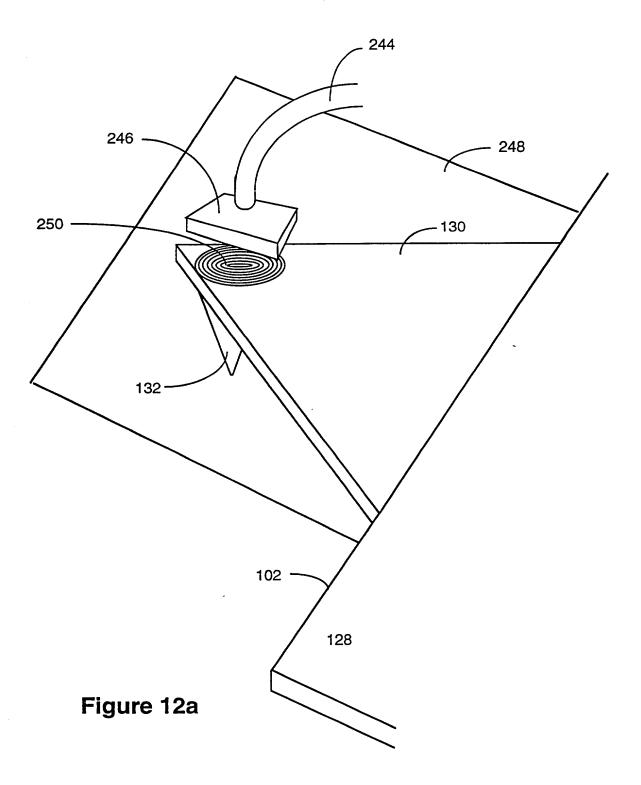
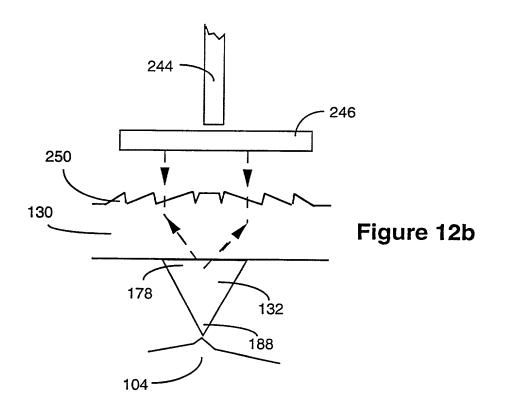
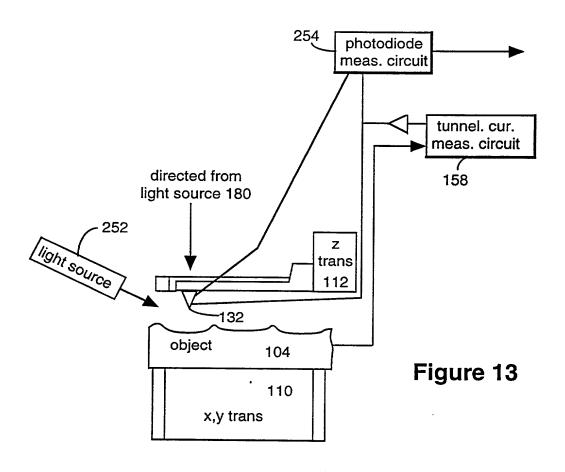


Figure 10b

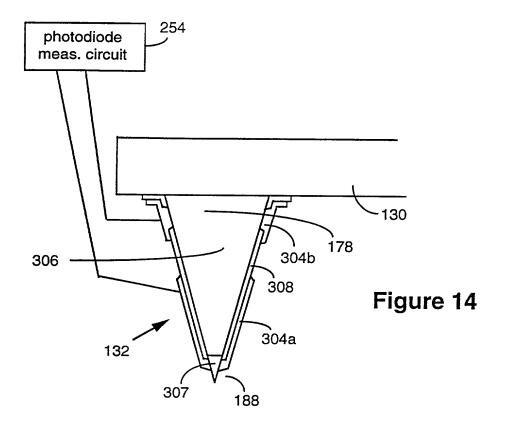


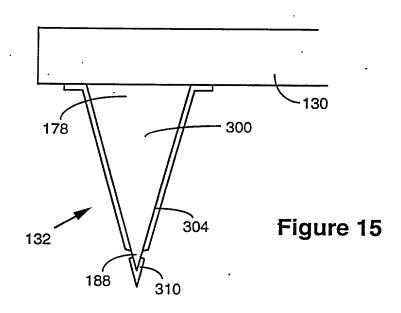


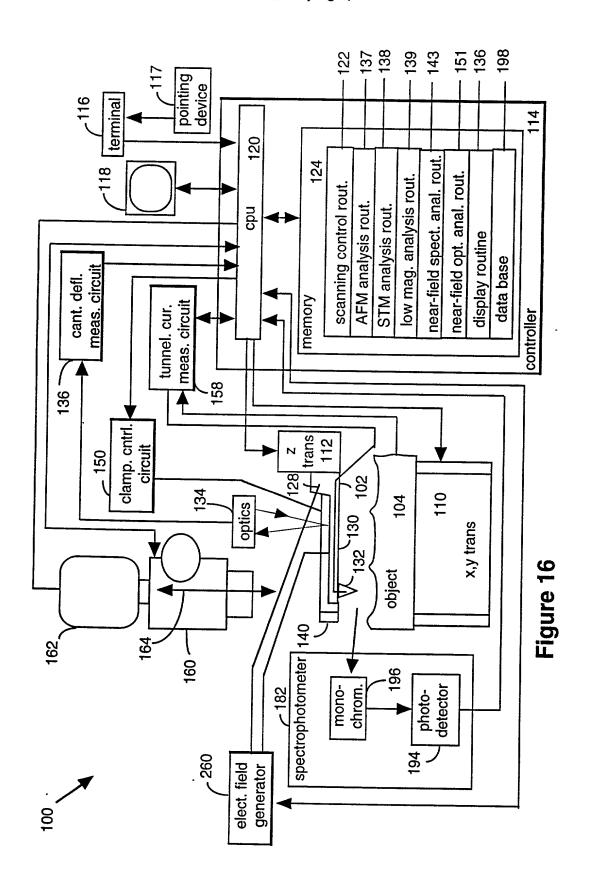




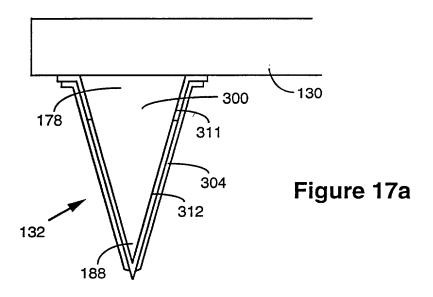
SUBSTITUTE SHEET (RULE 26)

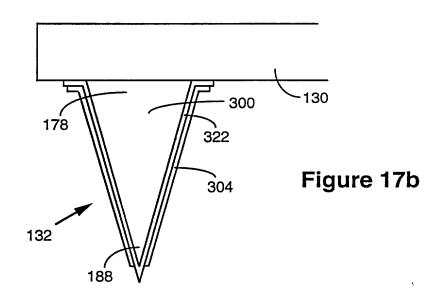


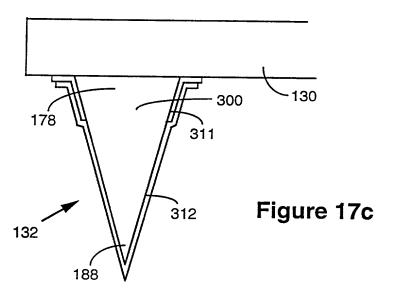


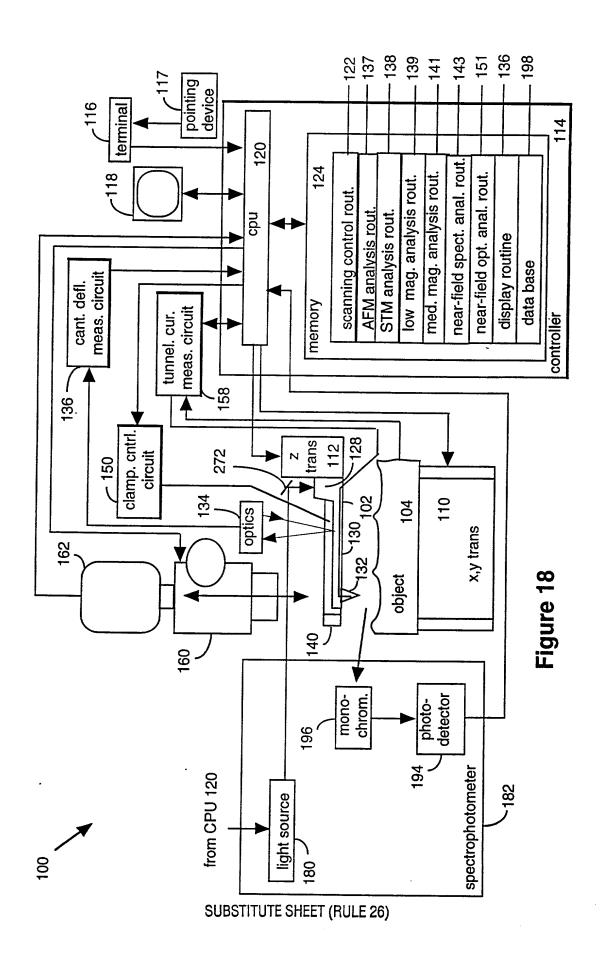


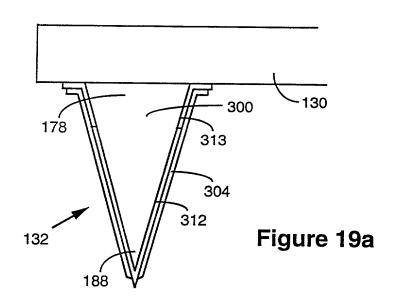
SUBSTITUTE SHEET (RULE 26)

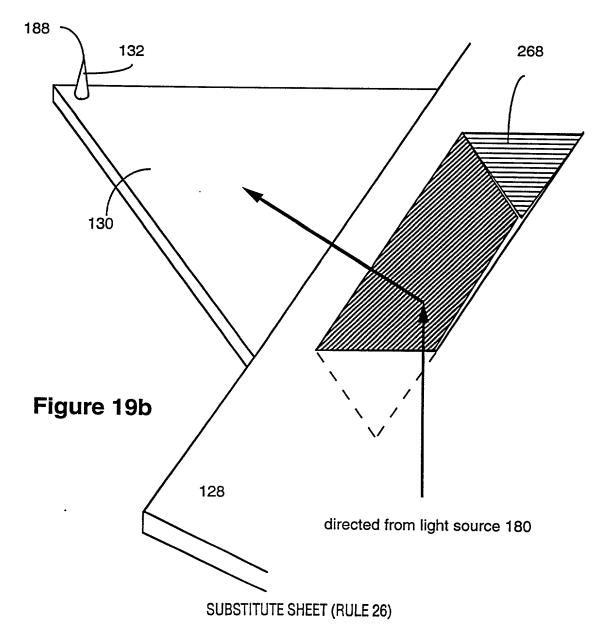


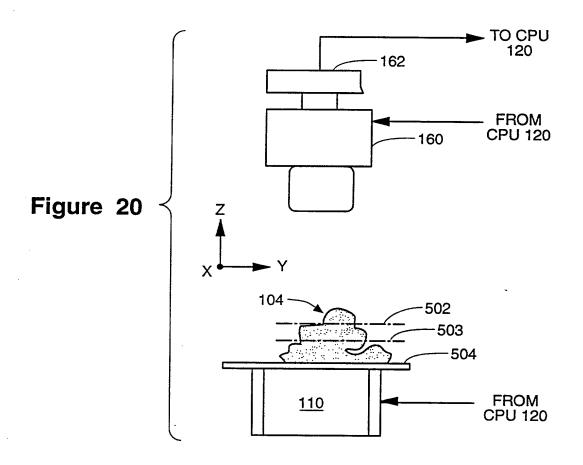


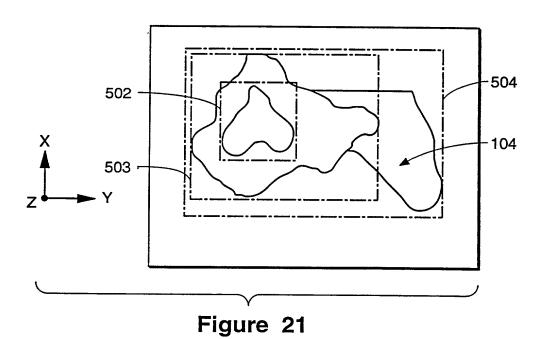












SUBSTITUTE SHEET (RULE 26)

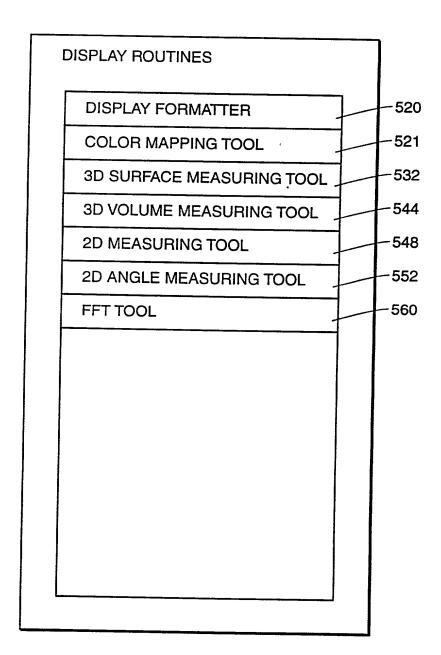
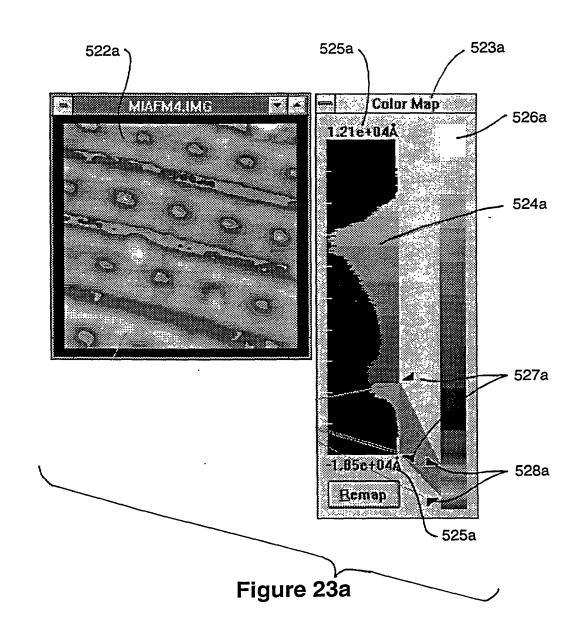


Figure 22



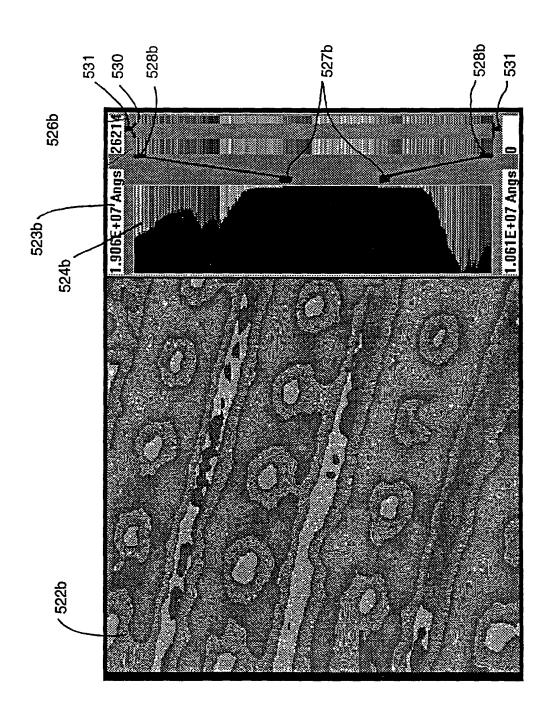


Figure 23b

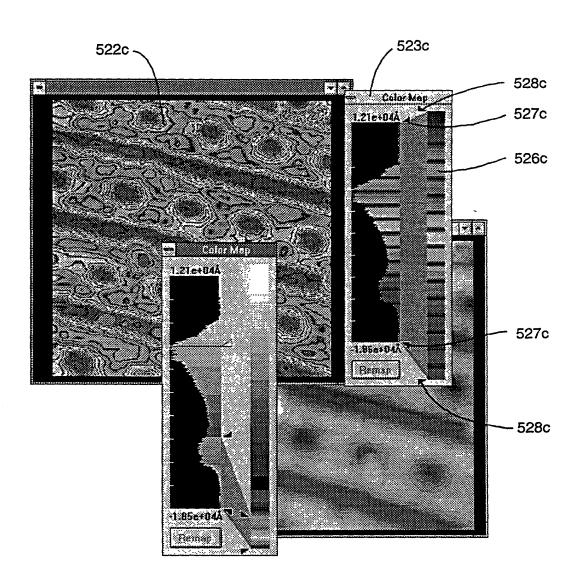


Figure 23c

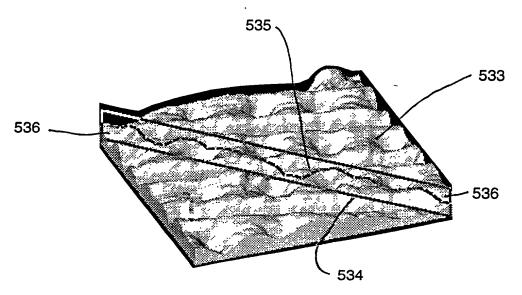
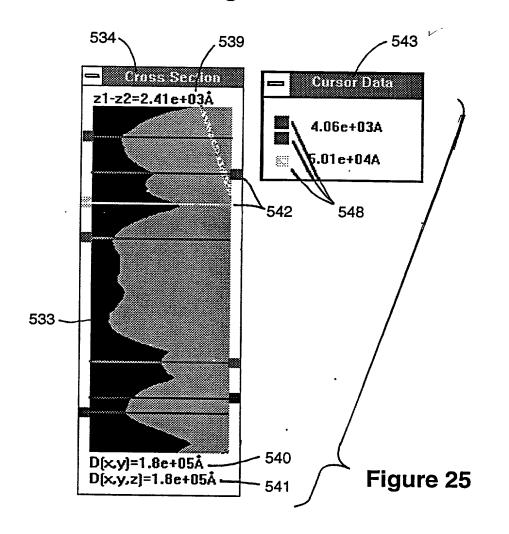


Figure 24



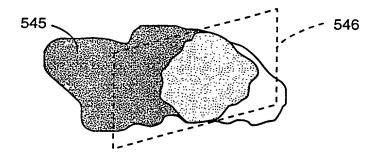


Figure 26

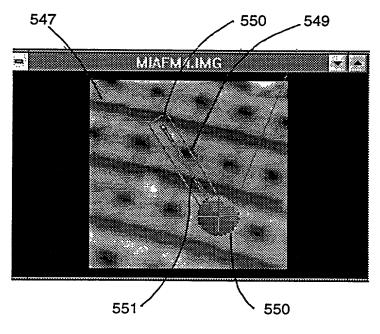


Figure 27

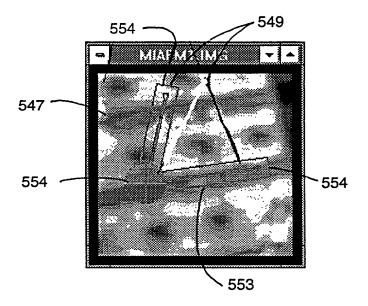


Figure 28

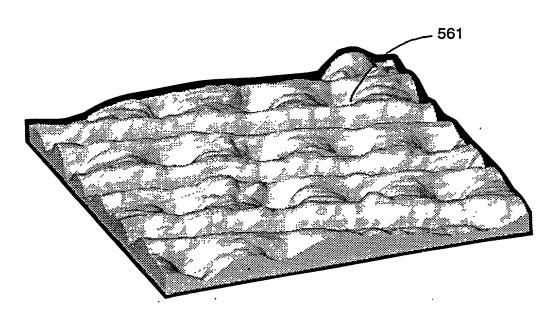
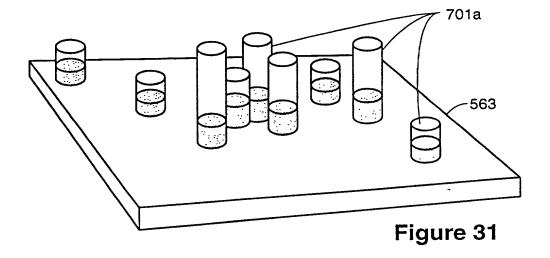
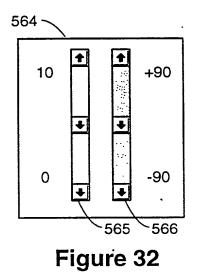


Figure 29





SUBSTITUTE SHEET (RULE 26)

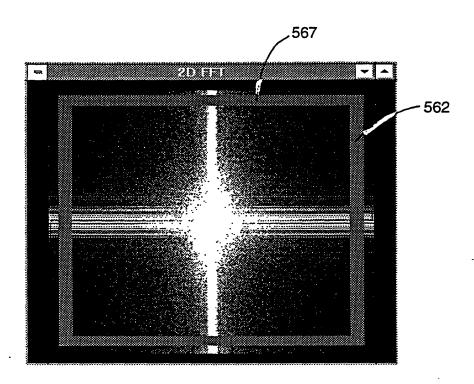


Figure 30

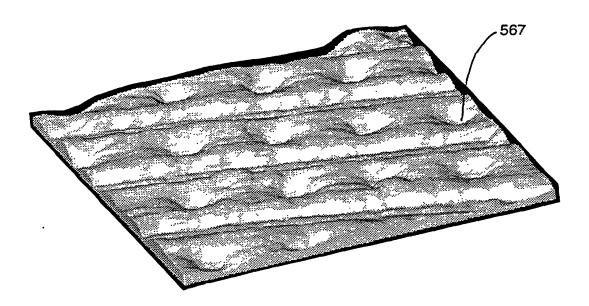


Figure 33
SUBSTITUTE SHEET (RULE 26)

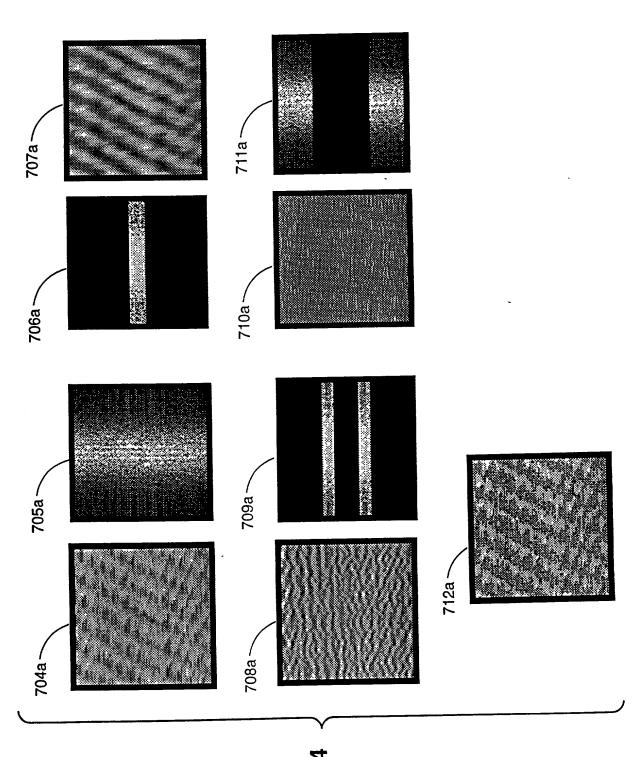


Figure 34

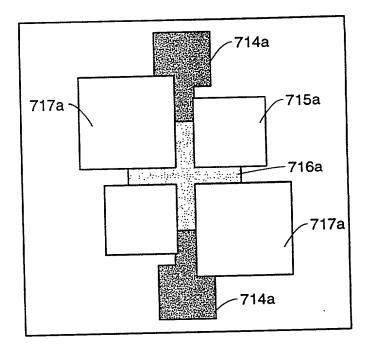


Figure 35

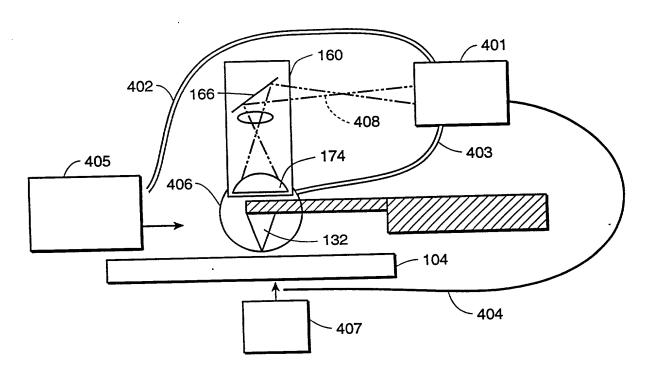


Figure 36

